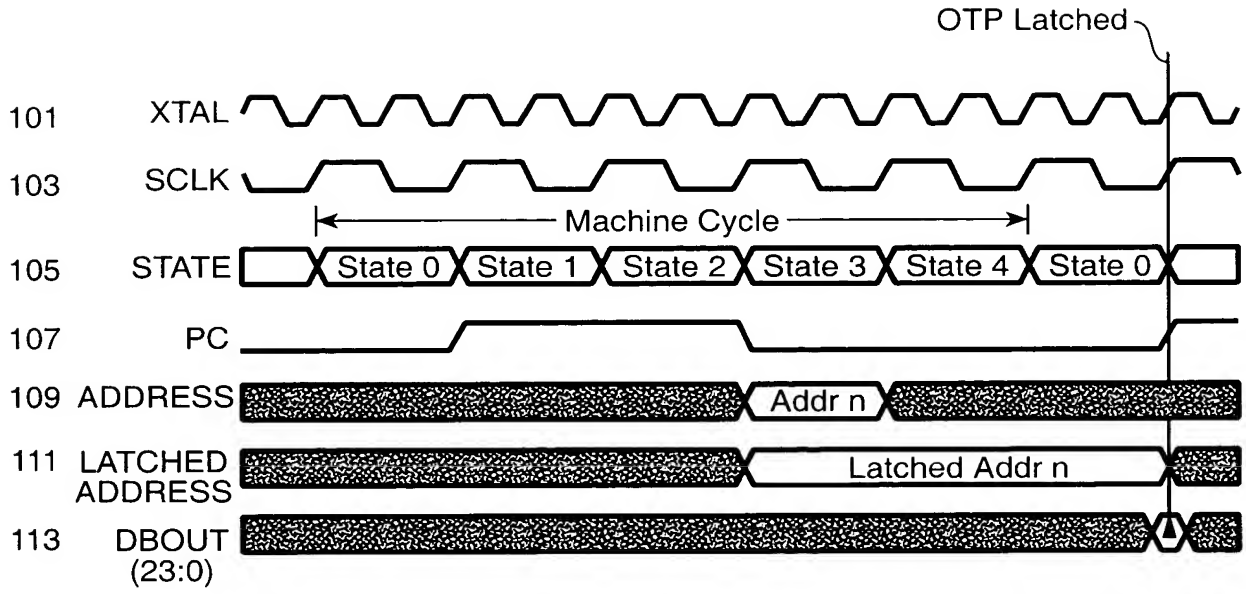

FIG._1

Logical Address	Memory - Location
0	OTP 0 - Address 0
1	OTP 1 - Address 0
2	OTP 2 - Address 0
3	OTP 3 - Address 0
4	OTP 0 - Address 1
5	OTP 1 - Address 1
6	OTP 2 - Address 1
Etc.	Etc.

FIG._2

OTP 3				OTP 2				OTP 1				OTP 0				
31	27	23	19	30	26	22	18	29	25	21	17	28	24	20	16	Row 1
7	6	5	4	7	6	5	4	7	6	5	4	7	6	5	4	
15	11	7	3	14	10	6	2	13	9	5	1	12	8	4	0	Row 0
3	2	1	0	3	2	1	0	3	2	1	0	3	2	1	0	

FIG._3

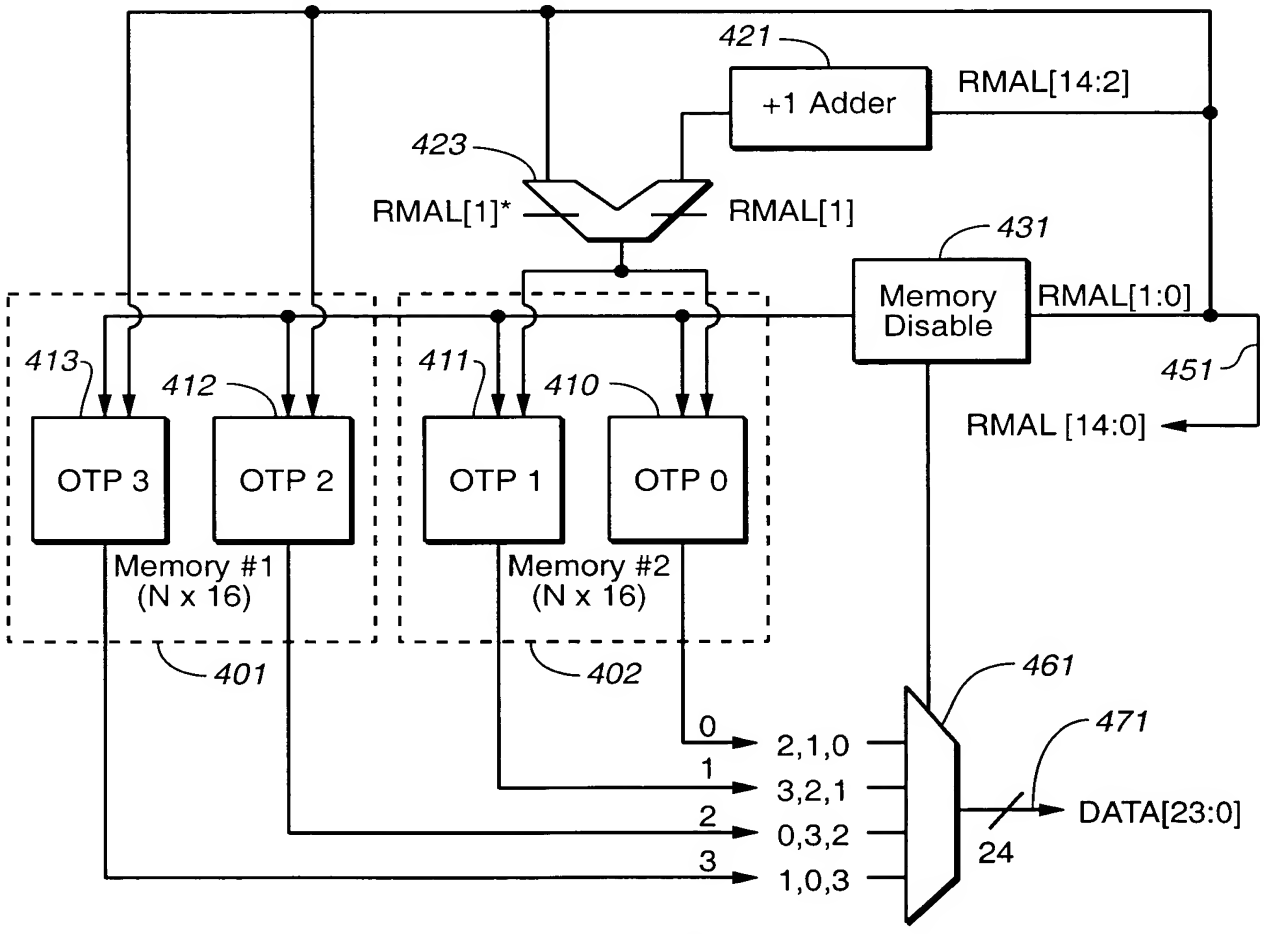

FIG._4

	OTP 3		OTP 2		OTP 1		OTP 0		
Add -	+1	+0	+1	+0	+1	+0	+1	+0	RMAL(4:0)
	X	X		2		1		0	00000
		3		2		1	X	X	00001
		3		2	X	X	4		00010
		3	X	X	5		4		00011
	X	X		6		5		4	00100
		7		6		5	X	X	00101
		7		6	X	X	8		00110
		7	X	X	9		8		00111
	X	X		10		9		8	01000
		11		10		9	X	X	01001
		11		10	X	X	12		01010
		11	X	X	13		12		01011
	X	X		14		13		12	01100
		15		14		13	X	X	01101
		15		14	X	X	16		01110
		15	X	X	17		16		01111
	X	X		18		17		16	10000
		19		18		17	X	X	10001
		19		18	X	X	20		10010
		19	X	X	21		20		10011
	X	X		22		21		20	10100
		23		22		21	X	X	10101
		23		22	X	X	24		10110
		23	X	X	25		24		10111
	X	X		26		25		24	11000
		27		26		25	X	X	11001
		27		26	X	X	28		11010
		27	X	X	29		28		11011
	X	X		30		29		28	11100
		31		30		29	X	X	11101
		31		30	X	X	32		11110
		31	X	X	33		32		11111

where **X** is a "don't care" state

FIG._5